CSE 548: Analysis of Algorithms

Lecture 24 (Analyzing I/O and Cache Performance)

Rezaul A. Chowdhury

Department of Computer Science SUNY Stony Brook Fall 2012

Memory: Fast, Large & Cheap!

For efficient computation we need

- fast processors
- fast and large (but not so expensive) memory

But memory <u>cannot be cheap, large and fast</u> at the same time, because of

- finite signal speed
- lack of space to put enough connecting wires

A reasonable compromise is to use a *memory hierarchy*.

The Memory Hierarchy



A memory hierarchy is

- almost as fast as its fastest level
- almost as large as its largest level
- inexpensive

The Memory Hierarchy



To perform well on a memory hierarchy algorithms must have <u>high</u> <u>locality</u> in their memory access patterns.

The Two-level I/O Model

The *two-level I/O (or cache-aware) model* [Aggarwal & Vitter, CACM'88] consists of:

- an *internal memory* of size M
- an arbitrarily large *external memory* partitioned into blocks of size *B*.



I/O complexity of an algorithm

= number of blocks transferred between these two levels

Basic I/O complexities: $scan(N) = \Theta\left(\frac{N}{B}\right)$ and $sort(N) = \Theta\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$

Algorithms often crucially depend on the knowledge of M and B \Rightarrow algorithms do not adapt well when M or B changes

The Ideal-Cache Model

The *ideal-cache model* [Frigo et al., FOCS'99] is an extension of the I/O model with the following additional feature:

algorithms for this model are not allowed to use knowledge of M and B.

CPU Cache Lines internal memory (size = M) Cache Misses block transfer (size = B) external memory

Consequences of this extension

- algorithms can simultaneously adapt to all levels of a multi-level memory hierarchy
- algorithms become more flexible and portable

Algorithms for this model are known as *cache-oblivious algorithms*.

- Optimal offline cache replacement policy
- **Exactly two levels of memory**
- □ Automatic replacement & full associativity

- **Optimal offline cache replacement policy**
 - LRU & FIFO allow for a constant factor approximation of optimal
 [Sleator & Tarjan, JACM'85]
- Exactly two levels of memory
- Automatic replacement & full associativity

- **Optimal offline cache replacement policy**
- **Exactly two levels of memory**
 - can be effectively removed by making several reasonable
 assumptions about the memory hierarchy [Frigo et al., FOCS'99]
- Automatic replacement & full associativity

- **Optimal offline cache replacement policy**
- Exactly two levels of memory
- □ Automatic replacement & full associativity
 - in practice, cache replacement is automatic (by OS or hardware)
 - fully associative LRU caches can be simulated in software with only a constant factor loss in expected performance [Frigo et al., FOCS'99]

The model makes the following assumptions:

- Optimal offline cache replacement policy
- **Exactly two levels of memory**
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Often makes the following assumption, too:

 \square $M = \Omega(B^2)$, i.e., the cache is *tall*

The model makes the following assumptions:

- Optimal offline cache replacement policy
- **Exactly two levels of memory**
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Often makes the following assumption, too:

- \square $M = \Omega(B^2)$, i.e., the cache is *tall*
 - most practical caches are tall

The Ideal-Cache Model: I/O Bounds

Cache-oblivious vs. cache-aware bounds:

□ Basic I/O bounds (same as the cache-aware bounds):

$$- scan(N) = \Theta\left(\frac{N}{B}\right)$$
$$- sort(N) = \Theta\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$$

- Most cache-oblivious results match the I/O bounds of their cacheaware counterparts
- There are few exceptions; e.g., no cache-oblivious solution to the *permutation* problem can match cache-aware I/O bounds
 [Brodal & Fagerberg, STOC'03]

Some Known Cache Aware / Oblivious Results

Problem	Cache-Aware Results	Cache-Oblivious Results
Array Scanning (<i>scan</i> (<i>N</i>))	$O\left(\frac{N}{B}\right)$	$O\left(\frac{N}{B}\right)$
Sorting (sort(N))	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B} ight)$
Selection	O(scan(N))	O(scan(N))
B-Trees [Am] (Insert, Delete)	$O\left(\log_B \frac{N}{B}\right)$	$O\left(\log_B \frac{N}{B}\right)$
Priority Queue [Am] (Insert, Weak Delete, Delete-Min)	$O\left(rac{1}{B}\log_{rac{M}{B}}rac{N}{B} ight)$	$O\left(\frac{1}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$
Matrix Multiplication	$O\left(\frac{N^3}{B\sqrt{M}}\right)$	$O\left(\frac{N^3}{B\sqrt{M}}\right)$
Sequence Alignment	$O\left(\frac{N^2}{BM}\right)$	$O\left(\frac{N^2}{BM}\right)$
Single Source Shortest Paths	$O\left(\left(V+\frac{E}{B}\right)\cdot\log_2\frac{V}{B}\right)$	$O\left(\left(V + \frac{E}{B}\right) \cdot \log_2 \frac{V}{B}\right)$
Minimum Spanning Forest	$O\left(\min\left(sort\left(E\right)\log_{2}\log_{2}V, V+sort\left(E\right)\right)\right)$	$O\left(\min\left(\operatorname{sort}(E)\log_2\log_2\frac{VB}{E}, V + \operatorname{sort}(E)\right)\right)$

<u>Table 1</u>: **N** = #elements, **V** = #vertices, **E** = #edges, Am = Amortized.

Matrix Multiplication

Matrix Multiplication

$$\begin{bmatrix}
 z_{ij} = \sum_{k=1}^{n} x_{ik} y_{kj} \\
 z_{11} \quad z_{12} \quad \cdots \quad z_{1n} \\
 z_{21} \quad z_{22} \quad \cdots \quad z_{2n} \\
 \vdots \quad \vdots \quad \ddots \quad \vdots \\
 z_{n1} \quad z_{n2} \quad \cdots \quad z_{nn}
 \end{bmatrix} = \begin{bmatrix}
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 x_{21} \quad x_{22} \quad \cdots \quad x_{2n} \\
 \vdots \quad \vdots \quad \ddots \quad \vdots \\
 x_{n1} \quad x_{n2} \quad \cdots \quad x_{nn}
 \end{bmatrix} \times \begin{bmatrix}
 y_{11} \quad y_{12} \quad \cdots \quad y_{1n} \\
 y_{21} \quad y_{22} \quad \cdots \quad y_{2n} \\
 \vdots \quad \vdots \quad \ddots \quad \vdots \\
 y_{n1} \quad y_{n2} \quad \cdots \quad y_{nn}
 \end{bmatrix}$$

$$Iter-MM(X, Y, Z, n) \\
 1. for \ i \leftarrow 1 \ to \ n \ do \\
 3. for \ k \leftarrow 1 \ to \ n \ do \\
 4. z_{ij} \leftarrow z_{ij} + x_{ik} \times y_{kj}
 \end{bmatrix}$$

I/O-Complexity: Iter-MM



Each iteration of the <u>for loop in line 3</u> incurs O(n) cache misses.

I/O-complexity of *Iter-MM* = $O(n^3)$

I/O-Complexity: Iter-MM



Block Matrix Multiplication



I/O-Complexity: Block-MM



Choose
$$s = \Theta(\sqrt{M})$$
, so that X_{ik} , Y_{kj} and Z_{ij} just fit into the cache.
Then line 4 incurs $\Theta\left(s\left(1+\frac{s}{B}\right)\right)$ cache misses.

I/O-complexity of *Block-MM* [assuming a *tall cache*, i.e., $M = \Omega(B^2)$]

$$= \Theta\left(\left(\frac{n}{s}\right)^{3}\left(s + \frac{s^{2}}{B}\right)\right) = \Theta\left(\frac{n^{3}}{s^{2}} + \frac{n^{3}}{Bs}\right) = \Theta\left(\frac{n^{3}}{M} + \frac{n^{3}}{B\sqrt{M}}\right) = \Theta\left(\frac{n^{3}}{B\sqrt{M}}\right)$$

(Optimal: Hong & Kung, STOC'81)

Multiple Levels of Cache



Multiple Levels of Cache



Multiple Levels of Cache



Recursive Matrix Multiplication



$$= \underbrace{\begin{array}{c} & & & & & \\ \uparrow & & & \\ n/2 & & & \\ \downarrow & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & &$$

Recursive Matrix Multiplication



Rec-MM(X, Y, Z, n)

- 1. if n = 1 then $Z \leftarrow Z + X \cdot Y$
- 2. else
- 3. Rec-MM (X_{11} , Y_{11} , Z_{11} , n/2), Rec-MM (X_{12} , Y_{21} , Z_{11} , n/2)
- 4. Rec-MM (X_{11} , Y_{12} , Z_{12} , n/2), Rec-MM (X_{12} , Y_{22} , Z_{12} , n/2)
- 5. Rec-MM (X_{21} , Y_{11} , Z_{21} , n/2), Rec-MM (X_{22} , Y_{21} , Z_{21} , n/2)
- 6. Rec-MM (X_{21} , Y_{12} , Z_{22} , n/2), Rec-MM (X_{22} , Y_{22} , Z_{22} , n/2)

I/O-Complexity: Rec-MM

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 then $Z \leftarrow Z + X \cdot Y$
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3. Rec-MM(X_{11} , Y_{11} , Z_{11} , $n/2$), Rec-MM(X_{12} , Y_{21} , Z_{11} , $n/2$)
4. Rec-MM(X_{11} , Y_{12} , Z_{12} , $n/2$), Rec-MM(X_{12} , Y_{22} , Z_{12} , $n/2$)
5. Rec-MM(X_{21} , Y_{11} , Z_{21} , $n/2$), Rec-MM(X_{22} , Y_{21} , Z_{21} , $n/2$)
6. Rec-MM(X_{21} , Y_{12} , Z_{22} , $n/2$), Rec-MM(X_{22} , Y_{22} , Z_{22} , $n/2$)

I/O-complexity of *Rec-MM*,
$$I(n) = \begin{cases} O\left(n + \frac{n^2}{B}\right), & \text{if } n^2 \le \alpha M \\ 8I\left(\frac{n}{2}\right) + O(1), & \text{otherwise} \end{cases}$$
$$= O\left(\frac{n^3}{M} + \frac{n^3}{B\sqrt{M}}\right) = O\left(\frac{n^3}{B\sqrt{M}}\right), \text{ when } M = \Omega\left(B^2\right)$$
(Optimal: Hong & Kung, STOC'81

Searching (Static B-Trees)



- □ A perfectly balanced binary search tree
- □ Static: no insertions or deletions
- $\Box \quad \text{Height of the tree, } h = \Theta(\log_2 n)$



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- $\Box \quad \text{Height of the tree, } h = \Theta(\log_2 n)$
- □ A search path visits O(h) nodes, and incurs $O(h) = O(\log_2 n)$ I/Os



- **\Box** Each node stores *B* keys, and has degree *B* + 1
- **u** Height of the tree, $h = \Theta(\log_B n)$



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Cache-Oblivious Static B-Trees?

<u>van Emde Boas Layout</u> h a binary search tree

van Emde Boas Layout



<u>van Emde Boas Layout</u>



van Emde Boas Layout



van Emde Boas Layout



and $k = \Theta(\sqrt{n})$

<u>van Emde Boas Layout</u>



and $k = \Theta(\sqrt{n})$

I/O-Complexity of a Search



I/O-Complexity of a Search



Sorting (Distribution Sort)

Cache-Complexity of Sorting

<u>Algorithm</u>	Cache-Complexity
Traditional (e.g., mergesort and heapsort)	O(NlogN)
Cache-Aware (e.g., external-memory versions of mergesort and distribution sort)	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$
Cache-Oblivious (e.g. funnelsort, cache-oblivious distribution sort and proximity mergesort)	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$

Cache-Complexity of Sorting

Algorithm	Cache-Complexity	
Traditional (e.g., mergesort and heapsort)	$O\left(\frac{N}{B}\log_2 N\right)$	
Cache-Aware (e.g., external-memory versions of mergesort and distribution sort)	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$	
Cache-Oblivious (e.g. funnelsort, cache-oblivious distribution sort and proximity mergesort)	$O\left(\frac{N}{B}\log_{\frac{M}{B}}\frac{N}{B}\right)$	
	optimal ——	1

Cache-Oblivious Distribution Sort

Step 1: Partition, and recursively sort partitions.

<u>Step 2</u>: Distribute partitions into buckets.

<u>Step 3</u>: Recursively sort buckets.

Step 1: Partition & Recursively Sort Partitions



Order:

Step 2: Distribute to Buckets

Recursively Sorted

Distributed to Buckets



Step 3: Recursively Sort Buckets

Recursively Sort Each Bucket



Done!

Distribution Sort

Step 1: Partition, and recursively sort partitions.

<u>Step 2</u>: Distribute partitions into buckets.

<u>Step 3</u>: Recursively sort buckets.

The Distribution Step



- We can take the partitions one by one, and distribute all elements of current partition to buckets
- **Has very poor cache performance:** $\Theta(\sqrt{n} \times \sqrt{n}) = \Theta(n)$ I/Os



Recursive Distribution



ignore
the cost of splits
for the time being

Let R(m, d) denote the cache misses incurred by *Distribute* (*i*, *j*, *m*) that copies *d* elements from *m* partitions to *m* buckets. Then

$$R(m,d) = \begin{cases} O\left(B + \frac{d}{B}\right), & \text{if } m \le \alpha B, \\ \sum_{1 \le i \le 4} R\left(\frac{m}{2}, d_i\right), & \text{otherwise, where } d = \sum_{1 \le i \le 4} d_i \\ = O\left(B + \frac{m^2}{B} + \frac{d}{B}\right) \end{cases}$$

$$R\left(\sqrt{n}, n\right) = O\left(\frac{n}{B}\right)$$

Recursive Distribution



→ ignore the cost of splits for the time being

Recursive Distribution



I/O-complexity of *Distribute* (1, 1, \sqrt{n}) is

$$= R\left(\sqrt{n}, n\right) + O\left(\frac{n}{B}\right) = O\left(\frac{n}{B}\right)$$

I/O-Complexity of Distribution Sort

- <u>Step 1</u>: Partition into \sqrt{n} sub-arrays containing \sqrt{n} elements each and sort the sub-arrays recursively.
- <u>Step 2</u>: Distribute sub-arrays into buckets B_1 , B_2 , ..., B_q .
- **Step 3:** Recursively sort the buckets.
- I/O-complexity of Distribution Sort:

$$Q(n) = \begin{cases} O\left(1 + \frac{n}{B}\right), & \text{if } n \le \alpha'M \\ \sqrt{n}Q\left(\sqrt{n}\right) + \sum_{i=1}^{q}Q\left(n_{i}\right) + O\left(1 + \frac{n}{B}\right), & \text{otherwise} \end{cases}$$
$$= O\left(\frac{n}{B}\log_{M}n\right), & \text{when } M = \Omega\left(B^{2}\right)$$